



**SHRI G.S. INSTITUTE OF TECHNOLOGY & SCIENCE**

( Government Aided Autonomous Institute, Established in 1952)

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**Recruitment of Junior Research Fellow (JRF) Under C2S Program**

No. Estt/Advt./2024/-

Date:18/04/2024

Online applications are invited from eligible candidates for appointment as Junior Research Fellow (JRF) under chip to startup project funded by MeitY, Govt. of India, on contract for one year on consolidated salary, latest by 17/05/2024. The tenure may be extended further upto 3 years or till project duration on the basis of performance as per project guidelines. Please visit the Institute's website [www.sgsits.ac.in](http://www.sgsits.ac.in) for further details and submitting the filled Application Form through email. Further amendments, if any, will be hosted on the Institute website only.

**DIRECTOR**

**Shri G. S. Institute of Technology and Science, Indore (MP)**  
(Government Aided Autonomous Institute Estd. In 1952)

**Department of Electronics and Instrumentation Engineering**

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**Advertisement for Post of Junior Research Fellow  
(On Contract)**

Under Chip-to-Startup (C2S) project funded by MeitY, Govt of India

Applications are invited from the eligible candidates for the position of Junior Research Fellow purely on contract basis for the project "Chip-to-Startup (C2S)" funded by Ministry of Electronics and Information Technology (MeitY) Government of India. The positions are contractual for fixed duration on fixed consolidated salary. The tenure may be extended further upto 4 years or till project duration on the basis of performance as per project guidelines. The positions shall be governed by the funding agency and service conditions of SGSITS Indore. The details of positions are as follows:

Title of Project	<b>Chip-to-Startup</b>
Funding Agency	<b>Ministry of Electronics and Information Technology (MeitY)</b>
Name of Position	JRF Junior Research Fellow
No. of Posts	02
Essential Qualification	B.E./B.Tech. in EI, ETC, EE & M.E./M. Tech. in Microelectronics & VLSI Design)+ GATE Qualification.
Experience	At least 2 year in VLSI related field.
Desirable	Working experience in Cadence/Siemens/Synopsis Tools.
Salary	Rs.42900/- per month Consolidated. Increment of 20% after completing two years subject to satisfactory performance and recommendation by review committee.

The scan copy of duly filled and signed application on the prescribed format along with self-attested copies of supporting documents (proof of date of birth, mark sheets/certificates/ documents in support of the essential qualifications and desirables etc.) should be sent to: [hodeisgits@gmail.com](mailto:hodeisgits@gmail.com)

on or before 17/05/2024

**Note:**

Shortlisted Candidates will be informed by e-mail about the interview date. So, the candidate must provide valid E-mail IDs in their application.

No TA/DA will be paid for appearing in the interview.

This appointment will be purely contractual. Initially appointment will be for first one year. It will be renewed for The tenure may be extended further upto 3 years or till project duration on the basis of performance as per project guidelines. or till the end of project whichever is earlier based on the performance of the candidate and recommendation of the selection/ review committee.