



(<http://ipindia.nic.in/index.htm>)



(<http://ipindia.nic>)

## Patent Search

Invention Title	NOVEL MINIATURIZED DUAL-PORT ULTRA WIDE BAND MIMO ANTENNA
Publication Number	52/2019
Publication Date	27/12/2019
Publication Type	INA
Application Number	201921052961
Application Filing Date	19/12/2019
Priority Number	
Priority Country	
Priority Date	
Field Of Invention	ELECTRONICS
Classification (IPC)	H01Q0009280000,H04B0007060000,H01Q0001380000,H01M0004587000,H04B0001000000

### Inventor

Name	Address	Country
Mr. Mohit Pant	PhD Scholar, Department of Electronics & Communication Engineering, Rajiv Gandhi Proudhyogiki Vishwavidyalaya (RGPV), Bhopal, MP	India
Dr. Mahesh Kumar Porwal	Dean (R&D) & Professor, Department of Electronics and Communication Engineering, Sree Chaitanya College of Engineering, Karimnagar (Telangana) - 505001, India	India
Mrs. Rashmi Pant	PhD Scholar, Department of Electronics & Communication Engineering, Rajiv Gandhi Proudhyogimki Vishwavidyalaya (RGPV), Bhopal, MP	India
Dr.Vineeta Chaudhary	Professor, Department of Electronics and Communication Engineering, Ujjain Engineering College, Ujjain.(MP), India	India
Dr. L.D Malviya	Associate Professor, Department of Electronics & Telecommunication Engineering, Shri G.S Institute of Science & Technology, Indore (MP), India	India
Dr. Manish Jain	Associate Professor, Electrical & Electronics Engineering Department, Mandsaur University, SH-31, Mhow Neemuch Bye-Pass Road, Mandsaur (MP) - 458001, India	India
Dr. Ashish Joshi	Professor & Head Dept of ECE/EX, BM College of Technology, Near Choukhi Dhani Khandwa Road, Indore (MP) - 452020, India	India
Pankaj Rathi	Associate Professor, Electronics & Communication Engineering, Shrinathji Institute of Technology & Engineering, Upali Oden, Nathdwara (Rajasthan), India	India
Rupesh Dubey	Associate Professor, Electronics & Communication Engineering, IPS Academy Institute of Engineering and Science, Knowledge Village Rajendra Nagar AB Road Indore (MP)- 452012, India	India
Dr. Jignesh Kumar Bhanubhai Jethva	Assistant professor, Electronics and Communication Engineering, Madhav University Madhav Hills, Opp. Banas River Bridge Toll N.H.-27, P.O. - Bharja, Abu Road, Pindwara, Rajasthan 307026, India	India

### Applicant

--

Name	Address	Country
Mr. Mohit Pant	PhD Scholar, Department of Electronics & Communication Engineering, Rajiv Gandhi Proudhyogiki Vishwavidyalaya (RGPV), Bhopal, MP	India
Dr. Mahesh Kumar Porwal	Dean (R&D) & Professor, Department of Electronics and Communication Engineering, Sree Chaitanya College of Engineering, Karimnagar (Telangana) – 505001, India	India
Mrs. Rashmi Pant	PhD Scholar, Department of Electronics & Communication Engineering, Rajiv Gandhi Proudhyogimki Vishwavidyalaya (RGPV) , Bhopal, MP	India
Dr.Vineeta Chaudhary	Professor, Department of Electronics and Communication Engineering, Ujjain Engineering College, Ujjain.(MP), India	India
Dr. L.D Malviya	Associate Professor, Department of Electronics &Telecommunication Engineering, Shri G.S Institute of Science & Technology, Indore (MP), India	India
Dr. Manish Jain	Associate Professor, Electrical & Electronics Engineering Department , Mandsaur University, SH-31, Mhow Neemuch Bye-Pass Road, Mandsaur (MP) – 458001, India	India
Dr. Ashish Joshi	Professor & Head Dept of ECE/EX , BM College of Technology , Near Choukhi Dhani Khandwa Road , Indore (MP) – 452020, India	India
Pankaj Rathi	Associate Professor, Electronics & Communication Engineering , Shrinathji Institute of Technology & Engineering, Upali Oden, Nathdwara (Rajasthan), India	India
Rupesh Dubey	Associate Professor, Electronics & Communication Engineering, IPS Academy Institute of Engineering and Science, Knowledge Village Rajendra Nagar AB Road Indore (MP)- 452012, India	India
Dr. Jignesh Kumar Bhanubhai Jethva	Assistant professor, Electronics and Communication Engineering, Madhav University Madhav Hills, Opp. Banas River Bridge Toll N.H.-27, P.O. - Bharja, Abu Road, Pindwara, Rajasthan 307026, India	India

#### Abstract:

The present invention relates to a novel miniaturized dual-port ultra wide band multiple input multiple output (MIMO ) antenna. The objective of the present invention overcome the inadequacies of the prior arts in ultra wide band (multiple input multiple output) MIMO antenna.

#### Complete Specification

#### Claims:

1. A miniaturized dual-port ultra wide band (multiple input multiple output (MIMO) antenna, wherein the miniaturized dual-port ultra wide band (multiple input output (MIMO) antenna comprises ,

A substrate, wherein the substrate containing the medium of Koch fractal slot structure floor;

A dielectric substrate front side and with the back of the dielectric substrate of the Koch fractal slot structure in the complementary structure of the chink;

A First Single Antenna comprises a full ground plane, designed on an FR4 substrate with a dielectric constant and height of the substrate (0.3 mm – 2.0 mm) with a defected ground structure; and

A Second Single Antenna comprises a full ground plane, designed on FR4 substrate with a dielectric constant and height of the substrate (0.3 mm – 2.0 mm) with a defected ground structure, wherein the First Single Antenna on the center of the base of the symmetrical distribution of the second Single Antenna radiation unit micro strip feed line and a micro strip feed line, micro strip feed line a and the micro strip feed line b are respectively connected to the First Single Antenna radiation unit of the second Single Antenna radiation unit.

2. The miniaturized dual-port ultra wide band (multiple input multiple output (MIMO) antenna as claimed in claim 1, wherein the First Single Antenna & Second Antenna working on UWB was replicated with one antenna rotated at 90 degrees with respect to other thus the two antennas were made orthogonal to reduce the

[View Application Status](#)



**Department of Industrial  
Policy and Promotion**  
Government of India

Terms & conditions (<http://ipindia.gov.in/terms-conditions.htm>) Privacy Policy (<http://ipindia.gov.in/privacy-policy.htm>)

Copyright (<http://ipindia.gov.in/copyright.htm>) Hyperlinking Policy (<http://ipindia.gov.in/hyperlinking-policy.htm>)

Accessibility (<http://ipindia.gov.in/accessibility.htm>) Archive (<http://ipindia.gov.in/archive.htm>) Contact Us (<http://ipindia.gov.in/contact-us.htm>)

Help (<http://ipindia.gov.in/help.htm>)

Content Owned, updated and maintained by Intellectual Property India, All Rights Reserved.

Page last updated on: 26/06/2019