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Patent Search

Invention Title	SYSTEM AND METHOD FOR MEASURING THE TECHNICAL EFFICIENCY OF ONE OR MORE INDUSTRIES
Publication Number	18/2022
Publication Date	06/05/2022
Publication Type	INA
Application Number	202211016244
Application Filing Date	23/03/2022
Priority Number	
Priority Country	
Priority Date	
Field Of Invention	COMPUTER SCIENCE
Classification (IPC)	G06Q0040040000, G06Q0010060000, G06Q0040000000, G09B0019000000, H04R0029000000

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Abstract:

Disclosed is a system (100) for measuring the technical efficiency of one or more industries that includes a memory (108) and a processor (110). The memory (108) stores a plurality of instructions; and the processor (110) executes the plurality of instructions. The processor (110) is configured to perform a plurality of operations comprising ste accessing performance of one or more Decision Making Units (DMUs) in Data Envelopment Analysis (DEA) using a concept of efficiency; estimating efficiencies using the D is relative to the best performing DMU; computing a relative efficiency to one or more groups observed the best practice which develops efficiency scores for all the DMUs scale from zero to hundred percent; and allowing input reducing and output increasing one or more orientations and assuming one or more constant returns to the scale a Charnes, Cooper, and Rhodes (CCR) model. The most illustrative drawing: FIG. 1

Complete Specification

Claims:WE CLAIM:

- 1. A method for measuring the technical efficiency of one or more industries, the method comprising:
- accessing, by one or more processors (110), performance of one or more Decision Making Units (DMUs) in Data Envelopment Analysis (DEA) using a concept of efficiency estimating, by the one or more processors (110), efficiencies using the DEA that is relative to the best performing DMU;
- computing, by the one or more processors (110), a relative efficiency to one or more groups observed the best practice which develops efficiency scores for all the DMUs a scale from zero to hundred percent; and
- allowing, by the one or more processors (110), input reducing and output increasing one or more orientations and assuming one or more constant returns to the scale through a Charnes, Cooper, and Rhodes (CCR) model.
- 2. The method as claimed in claim 1, wherein the concept efficiency is a ratio of total outputs to total inputs.
- 3. The method as claimed in claim 1, wherein the best performing DMU is assigned an efficiency score of unity or 100 percent, and the performance of other DMUs var between 0 and 100 percent relative to the best performance.
- 4. A system for measuring the technical efficiency of one or more industries, comprising:
- a memory (108) to store a plurality of instructions; and
- a processor (110) to execute the plurality of instructions, wherein the processor (110) is configured to perform a plurality of operations comprising steps of:
- accessing performance of one or more Decision Making Units (DMUs) in Data Envelopment Analysis (DEA) using a concept of efficiency

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Page last updated on: 26/06/2019