

**Shri G. S. Institute of Technology & Science Indore (M.P.)**

**Department of Electrical Engineering**

**TIME-TABLE**

**Jan - June 2024**

**Year M.TECH. (P.E.)**

**SEM - II**

**ROOM NO. - 103**

Time / Day	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday
09:00 to 10:00						
10:00 to 11:00			EE61612 (RSM)			
11:00 to 12:00	EE61613 (HC)		EE61614 (SS)		EE61613 (HC)	
12:00 to 1:00	EE61612 (RSM)	EE61614 (SS)	EE61711 (MPSC)	EE61711 (MPSC)	EE61612 (RSM)	
1:00 to 2:00	Recess					
2:00 to 3:00	EE61752 (AP)	EE61613 (HC)	EE61853 (Term Paper) (RSM) (PE Lab)	EE61855 (Minor Project & Lab-V) (SS) (PE Lab)	EE61752 (AP)	
3:00 to 4:00	EE61614 (SS)	EE61752 (AP)			EE61711 (MPSC)	
4:00 to 5:00					EE61854 (Lab-IV) (RR)	
5:00 to 6:00					(Software Lab)	

		L	T	P
EE61612	Power Electronics Supply Systems & Design	3		
EE61613	Integrated Power Electronics Converters	3		
EE61614	Solid State Control of Electrical Drives	3		
EE61711	Digital Signal Processing	3		
EE61752	Embedded System Programming	3		
EE61853	Term Paper			2
EE61854	LAB -IV			2
EE61855	Minor Project & Lab - V			4
		15		8

MPSC - Prof. M. P. S. Chawla

AP - Dr. Arun Parakh

SS - Dr. Shailendra Sharma

RSM - Mr. R. S. Mandloi

RR - Mrs. Rinki Rajpal

HC - Mr. Harshit Choubey

Head  
EED