

Shri G. S. Institute of Technology and Science, Indore

Minutes of Board of Studies of Electronics and Instrumentation Engineering

Date: 05/02/2021

A board of studies (BOS) meeting for Electronics and Instrumentation Engineering was held in the Department on 5th Feb 2021 at 3PM. External members attended the meeting On-line on Google meet platform. Following members were present.

1. Dr. R .S. Gamad	Chairman (BOS)
2. Dr. D. K. Mishra	Member
3. Dr. P. P. Bansod	Member
4. Mr. Pramod Jain	Member
5. Mr. D.S. Ajnar	Member
6. Mr. Rajesh Khatri	Member
7. Mr. R. C. Gurjar	Member
8. Dr. Santosh Vishwakarma	External Expert, IIT Indore
9. Mr. Anuj Upadhyay	External Expert (Industry)
10. Dr. Vaibhav Neema	Special Invitee, IET DAVV Indore

The deliberations of meeting are as under:

The minor changes (below 20%) in the syllabus of B. Tech. (Second, Third and Fourth year) Electronics and Instrumentation Engineering was discussed and approved in the meeting.

In the Scheme of B. Tech. third year V sem, the Elective-I: EI37251-Industrial Engineering Management should be read as **IP37251**.

In the Scheme of B. Tech. third year V sem, the CO34298: Artificial Intelligence is introduced in Elective-I.

In scheme of B. Tech. fourth year Seventh sem, BM47001: Medical Instrumentation is replaced with EI47XXX: VLSI Design (Elective-I) from eighth sem (Swap).

The Scheme of M. Tech. (Microelectronics and VLSI Design) as per AICTE norms is discussed and approved.

The minor changes (below 20%) in the syllabus of following M. Tech. subjects were discussed and approved.

(i) Design of Integrated Circuit

(ii) Semiconductor Devices and Process Modeling

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(iii) Communication RF IC Design

(iv) VLSI Test and Testability

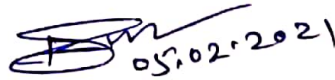
Following changes were also made in the Scheme of M. Tech. and are approved

- (1) Microelectronics is replaced by Hardware Descriptive Languages as a new subject.
- (2) VLSI Technology is replaced by Semiconductor Devices and Process modeling
- (3) Elective-I: Following electives are introduced.
 - (i) Analog IC Design,
 - (ii) Application Specific Integrated Circuits,
 - (iii) Memory Design and Testing
- (4) Elective-II: Following electives are introduced.
 - (i) VLSI Technology,
 - (ii) Micro-Electromechanical Systems (MEMS) Structure,
 - (iii) Sensors and Applications, SOC Design
- (5) Elective-III: Following electives are introduced.
 - (i) System Hardware Design,
 - (ii) Embedded System Design,
 - (iii) VLSI Signal Processing
- (6) Elective-IV: Following electives are introduced.
 - (i) Low Power VLSI Design,
 - (ii) Nanotechnology,
 - (iii) Optoelectronics Integrated Circuits

The meeting ended with vote of thanks.

Signature of members of BOS members:

1. Dr. R .S. Gamad



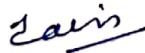
2. Dr. D. K. Mishra



3. Dr. P. P. Bansod



3. Mr. Pramod Jain




4. Mr. D.S. Ajnar

5. Mr. Rajesh Khatri



6. Mr. R. C. Gurjar



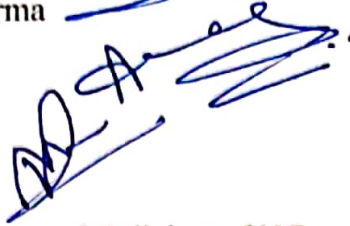
7. Dr. Santosh Vishwakarma



8. Mr. Anuj Upadhyay



9. Dr. Vaibhav Neema



Encl.: (i) Scheme and Approved Syllabus of UG
(ii) Scheme and Approved Syllabus of PG