

**One Week
Online Short Term
Training Programme
on**

**Mixed Signal & Radio
Frequency VLSI Design**

STTP 1:-23rd Nov. to 28th Nov. 2020

STTP 2:-07th Dec. to 12th Dec. 2020

STTP 3:-14th Dec. to 19th Dec. 2020

**Sponsored by
All India Council for Technical
Education, New Delhi**

**STTP Coordinator
Dr. R. S. Gamad**

Co-coordinator

STTP 1: Mr. Virendra Verma & Mr. R.R.Maharana

STTP 2: Dr. P. P. Bansod & Mr. R. C. Gurjar

STTP3: Mr. Rajesh Khatri & Mrs. Neha Pande

Organized by



**Department of Electronics & Instrumentation
Engineering
Shri G. S. Institute of Tech. and Science
23 Sir M. Visvesvaraya Marg, Indore,
Madhya Pradesh 452003**

PATRON

**Prof. Rakesh Saxena
DIRECTOR, SGSITS, INDORE
ADVISORY COMMITTEE**

Dr. D. K. Mishra

Dr. P. P. Bansod

Dr. Santosh Vishvakarma (IIT-Indore)

Dr. Vaibhav Neema (IET-DAVV)

STTP COORDINATOR

Dr. R. S. Gamad (Professor & Head, EIE)

CO-COORDINATOR

STTP 1: Mr. Virendra Verma & Mr. R.R.Maharana

STTP 2: Dr. P. P. Bansod & Mr. R. C. Gurjar

STTP3: Mr. Rajesh Khatri & Mrs. Neha Pande

ORGANIZING COMMITTEE

Mr. Pramod Kumar Jain	Mr. D. S. Ajnar
Ms. Neha Namdev	Mr. Arun Rayakwar
Ms. Neha Arya	Ms. Rupali Jarwal
Ms. Neha Gupta	Mr. Ritesh Gupta
Ms. Vineeta Viswakarma	Ms. Harshita Kushwaha
Mr. Nitesh Khatarkar	

ADDRESS FOR CORRESPONDANCE

**DR. R.S.GAMAD/MR. VIRENDRA K. VERMA/
MR. R. R. MAHARANA**

**Dept. of Electronics & Instrumentation Engg
Shri G. S. Institute of Technology and Sc.**

**23 Sir M. Visvesvaraya Marg, Indore,
Madhya Pradesh 452003**

Email: sgsits.eisttp@gmail.com

Mobile: +919826880333, +917972222500

About the Institute

Shri G. S. Institute of Technology and Science, Indore was established in 1952. SGSITS is recognized as a leading institute in the state of Madhya Pradesh and the western region consistently during its glorious journey of sixty years. It has been ranked among top 30-40 institutes on national level (including IITs and NITs) by independent bodies. Further looking to its credentials, All India Council for Technical Education (AICTE) and University Grant Commission (UGC), New Delhi, has declared the institute as autonomous in 1989. Under the autonomous state, the institute is affiliated to the Rajiv Gandhi Prodyogiki Vishwavidhyalaya (university of technology of M.P.) Bhopal. The institute offers ten UG courses (Regular), four UG courses (Part time), PG programs with twenty-two specializations, two part time PG courses in various disciplines under the emerging areas of Engineering, Technology, Applied Sciences, Computer Applications, Management & Pharmacy.

About the Department

The Department of Electronics & Instrumentation Engineering was established in 1984. The Department offers Bachelor of Engineering in Electronics & Instrumentation Engineering and Master of Technology in Electronics & Instrumentation with specialization in Microelectronics & VLSI Design. The Department has the excellent facilities for the research in field of RF IC design, System on chip design. Biomedical Signal processing. The department has well established laboratories in the related fields. Alumni of the Departments have pursued higher studies from various IITs and are placed in reputed organization.

Broad Topics to be covered in STTP

- Recent Developments in Mixed Signal VLSI Design
- Recent Developments in RF VLSI Design
- Device Modelling: MOS Devices (Double Gate, FinFETS etc.)
- CAD Tools, VLSI Interconnects, RFIC Design
- Digital System Design With FPGA
- FPGA in IOT

Resource Persons

Faculties from IITs, NITs, IIITs and other reputed institutes and experts from industries.



Workshop Objective

In Electronic industry has achieved a phenomenal growth over the last few decades, mainly due to the rapid advances in integration technologies and large-scale system design. The ability to design VLSI circuit is an increasingly important, if not essential, attribute of the professional engineer and electronics scientists. Indeed, the applications of VLSI promise to be widespread and the need to design will extend beyond the boundaries of electronics engineering into wide range of disciplines. Thus, there exists significant need for education in the VLSI in easily assimilated but yet comprehensive form. This course on “Mixed Signal & Radio Frequency VLSI Design” is well balanced and structured course specially designed to cover every aspect from fundamentals to chip design.

Participation

Faculty from Technical Institutions, Research Scholars (Ph.D and PG), Professionals working in Industry and Gov. Organizations are eligible to attend the Workshop. E-certificate will be provided on course completion to the participants with minimum 80 percent attendance and scored minimum 60 percent marks in quiz which has to be conducted at the end of the program.

Registration

Candidates should complete the registration through online in the provided link. Participants can register in any one STTP. Confirmation of eligible candidates will be on a first come first served basis. The confirmation will be sent to you to your registered mail id on or before 22/11/2020.

- No fee will be charged from participants.
- Link for registration: <https://forms.gle/XR2DvLCRwaRC2fTD7>
- Participants can register in any one STTP
- The brochure of the program may be downloaded from the Institute website www.sgsits.ac.in
- Last date of registration: 20.11.2020
- Confirmation will send to the registered mail id on or before 22.11.2020

