

SHRI G.S. INSTITUTE OF TECHNOLOGY & SCIENCE, INDORE

Department Of Electronics & Instrumentation Engg.



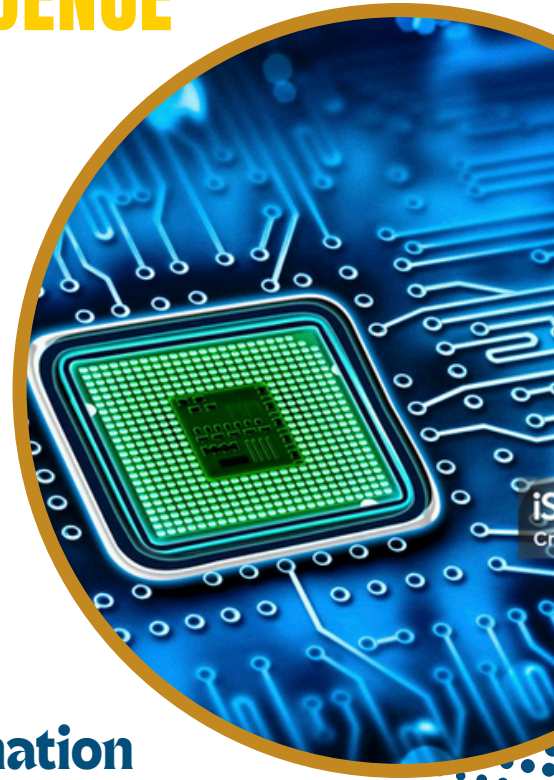
Four Week Summer Internship / Training / Certificate

CHIP DESIGN FLOW USING CADENCE

1 DURATION : 8TH JULY TO 2ND AUGUST 2024

2 ELIGIBILITY : 2ND YEAR & 3RD YEAR
MOVING STUDENTS

3 COURSE FEE : RS. 1500/-



Course Information

Introduction to Basic Flow of Cadance Tool.
Introduction to Basic CMOS Circuit Design.
Layout Design Rules.
RC Extraction Flow.
Post Layout Simulation.
Work On CMOS Based Project.



Payment Details:

a/c no. : 09111010000010
IFSC Code : PUNB0091110
Bank : Punjab National Bank
Branch : YN Road Indore-452003
Address : 23 YN Road Indore

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