

SHRI G.S. INSTITUTE OF TECHNOLOGY AND SCIENCE, INDORE
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING
 Academic year : 2024-25 Semester: Jan-June 2025
M.Tech (Micro-Electronics & VLSI Design)

Day Time	Monday	Tuesday	Wednesday	Thursday	Friday
9:00-10:00					
10:00-11:00	EI69705	EI69705		EI69753	
11:00-12:00		EI 69513	EI69511	EI69511	EI69753
12:00-1:00				EI69513	
1:00-2:00	LUNCH BREAK				
2:00-3:00		EI-69512	EI69512	PG Lab VLSI Design Lab-I	PG Lab VLSI Design Lab-II
3:00-4:00					
4:00-5:00					
5:00-6:00					

Subject Code	Subject Name	Faculty Incharge
EI69511	Communication RF IC Design	Dr. Rajesh Khatri
EI69512	Mixed Signal Circuit Design	Visiting Faculty
EI69513	VLSI Test & Testability	Dr. P.P. Bansod
EI69705	System Hardware Design	Dr. R. C. Gurjar
EI69753	Optoelectronic Integrated Circuits	Dr. G.G. Soni
PG Lab	VLSI Design Lab-I	Dr. Rajesh Khatri
PG Lab	VLSI Design Lab-II	Dr. R. C. Gurjar

Faculty Incharge
 Dr. R.C. Gurjar


 HOD, EI Dept.
 Dr. Rajesh Khatri