

SHRI G.S. INSTITUTE OF TECHNOLOGY AND SCIENCE, INDORE
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING

SESSION: 2025-26 Semester:(July-Dec) 2025

M. Tech.(Microelectronics and VLSI Design) I Year

Effective From : 12/08/2025

Time Table

Room No. Conference Room

Effective From : 12/08/2025					
Day	Monday	Tuesday	Wednesday	Thursday	Friday
Time					
9:00-10:00		↑ EI69454(Lab) ↓		↑ EI69014(L) ↓	
10:00-11:00	EI69012		EI69014		EI69012(L)
11:00-12:00	↑ EI69015(L) ↓		↑ EI69205 ↓		
12:00-1:00					
1-2 PM	LUNCH BREAK				
2:00-3:00	↑ EI69301(L) ↓	EI69015		↑ EI69453(LAB) ↓	
3:00-4:00		EI69301			
4:00-5:00					
5:00-6:00					

Subject Code	Subject Name	Faculty Incharge	Batches	Enrollment No
EI69012	Design of Integrated Circuits	Dr. Rajesh Khatri		
EI69014	Hardware Descriptive Languages	Dr. R. C. Gurjar		
EI69015	Semiconductor Devices and Process Modeling	Dr. P. P. Bansod		
EI69205	Analog Integrated Circuits	Mr. Neelesh Jain		
EI69301	VLSI Technology	Dr. Gireesh Soni		
EI69453	CAD Tool Design LAB-I	Dr. Rajesh Khatri		
EI69454	CAD Tool Design Lab-II	Dr. R. C. Gurjar		

Dr. R.C. Gurjar
Faculty Incharge


 Dr. Rajesh Khatri
 HOD (E & I Deptt)