

SHRI G.S. INSTITUTE OF TECHNOLOGY AND SCIENCE, INDORE
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING
SESSION: 2025-26 Semester: Jan-June 2026

1 year/II semester (PG M.Tech)

Effective From :

Room No. PG Classroom

Day Time	Monday	Tuesday	Wednesday	Thursday	Friday
9:00-10:00	VLSI Design Lab-III			VLSI Design Lab-IV	
10:00-11:00		EI69512	EI69511		
11:00-12:00		EI69753		EI69705	EI69513
12:00-1:00			EI69705	EI69753	
1:00-2:00	LUNCH BREAK				
2:00-3:00	EI69512	PG -II Seminar	EI69513	EI69511	Minor Project Lab- II
3:00-4:00	Minor Project Lab- II				
4:00-5:00				VLSI Design Lab-IV	
5:00-6:00					

Subject Code	Subject Name	Faculty Incharge
EI69511	Communication RF IC Design	Dr. Rajesh Khatri
EI69512	Mixed Signal Circuit Design	Prof. D.S. Ajnar
EI69513	VLSI Test & Testability	Dr. P.P. Bansod
EI69753	Optoelectronics Integrated Circuit	Dr. G. G. Soni
EI69705	System Hardware Design	Dr. R C Gurjar
	VLSI Design lab-IV	Dr. R. C. Gurjar/Ms. Neha Arya
	VLSI Design lab-III	Dr. Rajesh Khatri/Mr. Nilesh Jain
	Minor Project Lab-II	All Guides


HOD, EI Dept.