

Shri G.S. Institute Of Technology & Science, Indore



Department Of Electronics & Instrumentation Engg.

4 WEEK INTERNSHIP ON

CHIP DESIGN FLOW USING CADENCE

Duration :

3rd JUNE to 29th JUNE 2024 (4 Weeks)

Eligibility :

2nd Year (or 4th Sem) & 3rd Year (or 6th Sem)

Selection :

First Come Basis (Total 30 Seats)

Fees Details :

Rs 1500/- for Other Institutes

Rs 500/- for S.G.S.I.T.S Students

**Please Visit INSTITUTE WEBSITE for Program
Features And Time Schedule**

REGISTRATION LINK :

(<https://tinyurl.com/m8xpn4fe>)

Last Date to Register 31st May 2024

✉ hodeisgsits@gmail.com