

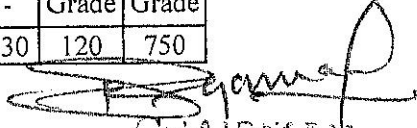
SHRI G S INSTITUTE OF TECHNOLOGY & SCIENCE, INDORE
I.M.TECH. MICROELECTRONICS & VLSI DESIGN

SEMESTER - I

S.No.	Subject Code	Subject	Hours per Week			Th. Credit	Pr. Credit	Maximum Marks				
			L	T	P			TH	CW	SW	Pr.	Total
1	E169012	Design of Integrated Circuits	3	-	-	3	-	70	30	-	-	100
2	E169014	Hardware Descriptive Language	3	-	-	3	-	70	30	-	-	100
3	E169015	Semiconductor Device and Process Modeling	3	-	-	3	-	70	30	-	-	100
4		Elective I	3	-	-	3	-	70	30	-	-	100
5		Elective II	3	-	-	3	-	70	30	-	-	100
6	E169453	Lab.- I : CAD Tool Design 1	-	-	4	-	2	-	-	40	60	100
7	E169454	Lab.- II : CAD Tool Design 2	-	-	4	-	2	-	-	40	60	100
8	E169500	Comprehensive Viva	-	-	-	-	-	-	-	-	Grade	Grade
Total			15	0	8	15	4	350	150	80	120	700

SEMESTER - II

S.No.	Subject Code	Subject	Hours per Week			Th. Credit	Pr. Credit	Maximum Marks				
			L	T	P			TH	CW	SW	Pr.	Total
1	E169511	Communication RF IC Design	3	-	-	3	-	70	30	-	-	100
2	E169512	Mixed Signal Circuit Design	3	-	-	3	-	70	30	-	-	100
3	E169513	VLSI Test and Testability	3	-	-	3	-	70	30	-	-	100
4		Elective III	3	-	-	3	-	70	30	-	-	100
5		Elective IV	3	-	-	3	-	70	30	-	-	100
6		Lab.- III : VLSI Design Lab.1	-	-	4	-	2	-	-	40	60	100
7		Lab.- IV : VLSI Design Lab.2	-	-	4	-	2	-	-	40	60	100
7		Minor Project / Lab - II	-	-	2	-	1	-	-	50	-	50
9	E169900	Comprehensive Viva	-	-	-	-	-	-	-	-	Grade	Grade
Total			15	0	10	15	5	350	150	130	120	750


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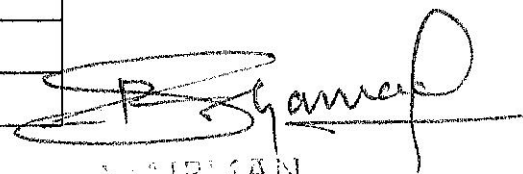
List of Electives

Elective I		Semester I
S.No	Sub. Code	Subject
1	EI69205	Analog IC Design
2	EI69211	Applicaton Specific Intregated Circuit
3	EI69206	Memory Design and Testing

Elective II		Semester I
S.No	Sub. Code	Subject
1	EI69301	VLSI Technology
2	EI69302	Micro- Electromechanical Systems (MEMS) Structure, Sensor and Application
3	EI69303	SOC Design

Elective III		Semester II
S.No	Sub. Code	Subject
1	EI69705	System Hardware Design
2	EI69706	Embedded System Design
3	EI69707	VLSI Signal Processing

Elective IV		Semester II
S.No	Sub. Code	Subject
1	EI69751	Low Power VLSI Design
2	EI69752	Nanotechnology
3	EI69753	Optoelectronic Integrated Circuits


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II M.TECH. MICROELECTRONICS & VLSI DESIGN

SEMESTER - III

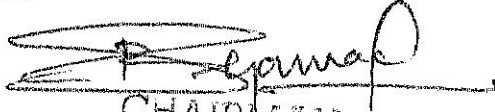
S.No.	Subject Code	Subject	Hours per Week			Th. Credit	Pr. Credit	Maximum Marks				
			L	T	P			TH	CW	SW	Pr.	Total
1	EI69931	*Industrial Training/Term Paper/Seminar	-	-	8	-	4	-	-	50	-	50
2	EI69932	Dissertation Phase-I	-	-	20	-	10	-	-	40	60	100
3	MA69902	**Research Methodology & IPR	2	-	-	2	-	70	30	-	-	100
Total			2	0	28	2	14	70	30	90	60	250

* Industrial training to be carried out between II and III semester during vacation

** This course can be studied online with prior permission of HOD, however, End-semester examination will be conducted in the Institute.

SEMESTER - IV

S.No.	Subject Code	Subject	Hours per Week			Th. Credit	Pr. Credit	Maximum Marks				
			L	T	P			TH	CW	SW	Pr.	Total
1	EI69952	Dissertation Phase-II	-	-	32	-	16	-	-	80	120	200
Total			0	0	32	0	16	0	0	80	120	200


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